

800.0039

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Pechanek, et al.

Serial No.: 09/422,015

Filed: October 21, 1999

For: METHODS AND APPARATUS FOR ABBREVIATED INSTRUCTION AND  
CONFIGURABLE PROCESSOR ARCHITECTURE

Group: 2183

Examiner: K. Kim

Chapel Hill, North Carolina  
April 23, 2001ASSISTANT COMMISSIONER FOR PATENTS  
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